

CLAIMS

I Claim:

- 1 1. A video graphics system comprising:
2 a transport stream port to receive a digital video transport stream, the digital video
3 transport stream including a data stream and control signals;
4 a transport stream interface control having an input coupled to the transport
5 stream port, and having a first output port to provide a set of control
6 signals, and a second output port to provide video graphics data; and
7 a data storage controller having a first port coupled to the first output port of the
8 transport stream interface control, an address port to provide an address
9 value, and a control port to provide control signals
- 1 2. The system of claim 1 further comprising:
2 a memory having a first port coupled to the second output port of the transport
3 stream interface, a second port coupled to the address port of the data
4 storage controller, and a third port coupled to the control port of the data
5 storage controller.
- 1 3. The system of claim 2, wherein the memory is a frame buffer memory.
- 1 4. The system of claim 2 further comprising:
2 a system bus/interface having a first port coupled to a fourth port of the memory,
3 a second port coupled to a fifth port of the memory.
- 1 5. The system of claim 4, wherein the fourth port of the memory is substantially the
2 same as the second port of the memory.
- 1 6. The system of claim 1, wherein the digital video transport stream is a digital video
2 broadcast transport stream.
- 1 7. The system of claim 6, wherein the control signals of the digital video transport stream
2 include a clock signal, a synchronization signal, and a data valid signal. It can also
3 contain an error signal, indicating there is an error in the transport stream.
- 1 8. The system of claim 7, wherein the set of control signals of the transport stream
2 interface control includes a start of field signal to indicate the start of a frame of
3 video.
- 1 9. The system of claim 8, wherein the set of control signals of the transport stream
2 interface control signal includes a valid data output to indicate when data on the
3 second output port of the transport stream interface control is active video data.

10. The system of claim 9, wherein the set of control signals of the transport stream interface control includes a valid vertical blanking interval signal to indicate when data on the second output port of the transport stream interface control is present during a vertical blanking interval.

11. The system of claim 6, further comprising:
a first video port to receive digital video of a first type, wherein the first type is not digital video broadcast stream video;
a first video interface control having an input coupled to the first video port, and having a first output port to provide the set of control signals, and a second output port to provide video graphics data; and
a select node coupled to the transport stream interface control and to the first video interface control.

12. The system of claim 11, wherein the first video port is a zoom video port.

13. The system of claim 11, wherein the first video port is a digital video stream port.

14. A method of receiving video graphics data, the method comprising the steps of:
receiving a transport stream associated with a digital video broadcast signal, the transport stream having data signals and control signals;
generating a secondary set of controls signals from the transport stream's control signals;
storing at least a portion of the transport stream data signals in a memory buffer controlled by the secondary set of control signals; and
sending the contents of the memory buffer to a system bus.

15. The method of claim 14, further comprising:
wherein the steps of receiving, generating and storing occur when in a first mode of operation;
during a second mode of operation, performing the steps of:
receiving a digital video signal having a data signals and a control signals, wherein the digital video signal is of a different type than the transport stream;
generating the secondary set of controls signals from the digital video signal's control signals; and
storing at least a portion of the digital video signal in the memory buffer based on the secondary set of control signals.

16. The method of claim 15, wherein the video signal is a Zoom Video signal.

17. The method of claim 14, wherein the memory buffer is a frame buffer.

1 18. A system for receiving a digital video broadcast signal, the system comprising:
2 a tuner to receive a digital video broadcast signal and to provide an analog output
3 signal;
4 a demodulator coupled to receive the analog output signal from the tuner, and to
5 provide a transport stream;
6 a video graphics adapter coupled to receive the transport stream and having a
7 system interface port, the video graphics adapter further includes a video
8 engine and a video output port.

1 19. The system of claim 18, wherein the video graphics adapter includes:
2 a memory to store at least a portion of the transport stream.

1 20. The system of claim 18, further comprising:
2 a central processor unit coupled to the system interface port of the video graphics
3 adapter; and
4 a transport demultiplexor coupled to demodulator.

1 21. A method of storing video data, the method comprising the steps of:
2 in a first mode of operation storing pixel information in a frame buffer of a video
3 adapter, wherein one line of frame buffer memory is representative of one
4 line of a video image to be displayed;
5 in a second mode of operation, storing compressed transport stream data in the
6 frame buffer, wherein one line of frame buffer memory is representative of
7 one transport stream packet.

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